

TRANSMITTAL OF FORMAL DRAWINGSDocket No.
CAF-27202/03In Re Application Of: **Richard Illman**

DEC 07 2004

Application No.

09/808,325

Filing Date

3/14/2001

Examiner

Guy J. Lamarre

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Group Art Unit

2133

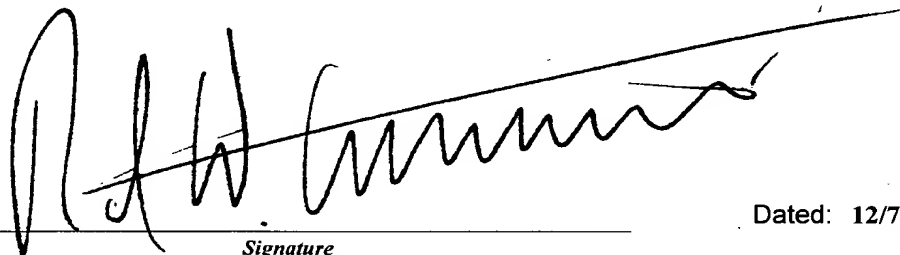
Confirmation No.

3865

Invention: **METHOD FOR TESTING FOR THE PRESENCE OF FAULTS IN DIGITAL CIRCUITS**Address to:
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Transmitted herewith are:

Seven (7) sheets of formal drawing(s) for this application.

☒ Each sheet of drawing indicates the identifying indicia suggested in 37 CFR Section 1.84(c).

Signature

Ronald W. Citkowski
Reg. No. 31,005
Gifford, Krass, Groh, Sprinkle,
Anderson & Citkowski, P.C.
280 N. Old Woodward Ave., Suite 400
Birmingham, MI 48009
248-647-6000

Dated: 12/7/2004

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to "Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450" [37 CFR 1.8(a)] on

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Janice Burkhardt
Janice Burkhardt